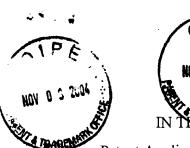
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IR-2132 (2-3575)

N TRADEMARK OFFICE

in re Patent Application of

Thomas J. Ribarich

Serial No.: 10/615,710

Filed: July 8, 2003

New York, New York

Date: October 29, 2004

Group Art Unit: 2821

Examiner: Ephrem Alemu

For: CHIP BALLAST CONTROL WITH POWER FACTOR CORRECTION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

SUBMISSION

Sir:

Submitted herewith is a copy of a search report issued by a patent searching authority other than the PTO and the cited art together with a form listing the same for the convenience of the Examiner.

I certify that each item of information contained in this Submission was cited in a communication from a foreign patent office in a counterpart foreign application and that this communication was not received by any individual designated in 37 C.F.R. 1.56(c) more than 30 days prior to the filing of this Submission.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450, on October 29, 2004

Brendan J. Kennedy

Name of applicant, assignee or Registered Representative

Registered Representative

Signature October 29, 2004

Date of Signature

Respectfully submitted,

Brendan J. Kennedy Registration No.: 41,890/

OSTROLENK, FABER, GERB & SOFFEN, LLP

1180 Avenue of the Americas

New York, New York 10036-8403

Telephone: (212) 382-0700

BJK:gl Enclosures

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